Errata Sheet: 2703A/AP CPU

Objective

This errata sheet provides details on problems encountered with the 2703A and 2703AP modules. Solutions to work around these problems are provided whenever possible.

Revision History

Use register 13003 to confirm the firmware revision. For example, version 3.05 will read as 305 in this register.

Firmware revision 3.08

There are no known issues at this time.

Firmware revision 3.05

- **Millisecond timer** Register 13002 gets reset to zero upon a system reset generated by communications or by turning on the dedicated reset input.
- **Communications port 1** This port periodically drops packets at 19200 baud or higher when communications port 0 is communicating at 19200 or higher.
- Reading from a 2207/2220 analog module When reading a negative voltage from a Model 2220 analog input that has the analog scaling register 13010 set to 1, an incorrect value will be returned on the analog input. Additionally, Model 2207 analog inputs are incorrectly scaled when register 13010 is set to 1.
- **PLS** When a 0 is stored to Register 5913 or 5923 for a PLS rollover, the PLS current position in Register 5912 or 5922 does not reset to 0.